



**PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q77191

Koichi OHTO, et al.

Appln. No.: 10/650,193

Group Art Unit: 2826

Confirmation No.: 6245

Examiner: Alexander O. WILLIAMS

Filed: August 28, 2003

For: SEMICONDUCTOR DEVICE HAVING SILICON-DIFFUSED METAL WIRING
LAYER AND ITS MANUFACTURING METHOD

RESPONSE TO RESTRICTION REQUIREMENT

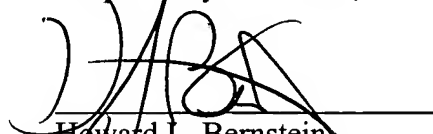
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 24, 2004 in which the Examiner has required a restriction, Applicant responds by electing Group I, *i.e.*, claims 1-53, drawn to a semiconductor device. This election is made without prejudice to the filing of a divisional application directed to the non-elected claims.

Prompt and favorable action on the elected claims is now respectfully requested.

Respectfully submitted,


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Date: April 26, 2004